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Complete if Known**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 1

Application Number	10/750,714
Filing Date	December 31, 2003
First Named Inventor	Ricardo E. Gonzalez
Art Unit	2143
Examiner Name	Unknown
Attorney Docket Number	PA2683US

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
B.S.		GOLDBLATT, K., "The Low-Cost, Efficient Serial Configuration of Spartan FPGAs," XAPPO98, November 13, 1998 (Version 1.0), XILINX.	
B.S.		SCOTT, S. et al., "The Cray T3E Network: Adaptive Routing in a High Performance 3D Torus," HOT Interconnects IV, August 15-16, 1996, Stanford University.	
B.S.		GOLESTARI, S.J., "A Stop-and-Go Queuing Framework for Congestion Management," Applications, Technologies, Architectures, and Protocols for Computer Communication, Proceedings of the ACM Symposium on Communications Architectures & Protocols, 1990, ACM Press, New York, NY USA.	
B.S.		DALLY, W. et al., "Deadlock Free Message Routing in Multiprocessor Interconnection Networks," Computer Science Department, California Institute of Technology, May 10, 1985.	
B.S.		GARLAND, D. et al., "An Introduction to Software Architecture," Advances in Software Engineering and Knowledge Engineering, 1993, Vol. 1, World Scientific Publishing Company, New Jersey, USA.	

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03/30/2006

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Application Number	10/750,714
Filing Date	December 31, 2003
First Named Inventor	Ricardo E. Gonzalez
Art Unit	2183
Examiner Name	Eddie P. Chan
Attorney Docket Number	PA2683US

Sheet	1	of	1
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U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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Examiner Signature		Date Considered	03/30/2006
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